TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC7SET125F,TC7SET125FU

#### **Bus Buffer**

#### **Features**

• High speed :  $t_{pd}$  = 3.7 ns (typ.)

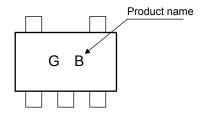
at  $V_{CC} = 5 \text{ V}$ ,  $C_L = 15 \text{pF}$ 

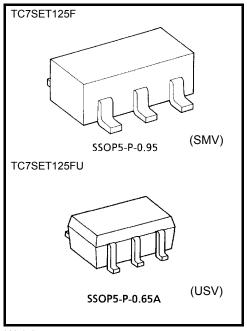
Low power dissipation : I<sub>CC</sub> = 2 μA (max) at Ta = 25°C

• Compatible with TTL outputs.:  $V_{IL}$  = 0.8 V (max)  $V_{IH}$  = 2.0 V (min)

• 5.5V tolerant input.

#### Marking





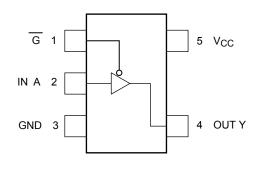
Weight

SSOP5-P-0.95 : 0.016 g (typ.) SSOP5-P-0.65A : 0.006 g (typ.)

#### Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage	V <sub>CC</sub>	-0.5 to 7.0	V
DC input voltage	V <sub>IN</sub>	–0.5 to 7.0	٧
DC output voltage	V <sub>OUT</sub>	-0.5 to V <sub>CC</sub> + 0.5	٧
Input diode current	l <sub>IK</sub>	-20	mA
Output diode current	lok	±20(Note 1)	mA
DC output current	lout	±25	mA
DC V <sub>CC</sub> /ground current	I <sub>CC</sub>	±50	mA
Power dissipation	P <sub>D</sub>	200	mW
Storage temperature	T <sub>stg</sub>	-65 to 150	°C
Lead temperature (10 s)	TL	260	°C

## Pin Assignment (top view)



Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note1:  $V_{OUT} < GND$ ,  $V_{OUT} > V_{CC}$ 

# IEC Logic Symbol



### **Truth Table**

G	Α	Υ
Н	Х	Z
L	L	L
L	Н	Н

X: Don't care

Z: High impedance

# **Operating Ranges**

Characteristics	Symbol	Rating	Unit
Supply voltage	V <sub>CC</sub>	4.5 to 5.5	V
Input voltage	V <sub>IN</sub>	0 to 5.5	V
Output voltage	V <sub>OUT</sub>	0 to Vcc	V
Operating temperature	T <sub>opr</sub>	–40 to 85	°C
Input rise and fall time	dt/dv	0 to 20	ns/V

### **DC Electrical Characteristics**

Characteristics Symbol		Test Condition			Ta = 25°C			Ta = -40 to 85°C		Unit
				V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Offic
High-level input voltage	V <sub>IH</sub>	_		4.5 to 5.5	2.0	_	_	2.0	_	V
Low-level input voltage	V <sub>IL</sub>	_		4.5 to 5.5	_	_	0.8	_	0.8	V
High lovel output voltage	Vau	$V_{IN} = V_{IH}$	$I_{OH} = -50 \mu A$	4.5	4.4	4.5	_	4.4	_	V
High-level output voltage	V <sub>OH</sub>	or V <sub>IL</sub>	$I_{OH} = -8 \text{ mA}$	4.5	3.94	_	_	3.80	_	V
Low-level output voltage V <sub>OL</sub>	Voi	V <sub>IN</sub> = V <sub>II</sub>	I <sub>OL</sub> = 50 μA	4.5	_	0.0	0.10	_	0.10	V
	VIN – VIL	I <sub>OL</sub> = 8 mA	4.5	_	_	0.36	_	0.44	V	
3-state output off-state current	l <sub>OZ</sub>	V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub> V <sub>OUT</sub> = V <sub>CC</sub> or GND		5.5	_	_	±0.25	_	±2.5	μА
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = 5.5 V or GND		0 to 5.5	_	_	±0.1	_	±1.0	μА
	Icc	V <sub>IN</sub> = V <sub>CC</sub> or GND		5.5	_	_	2.0	_	20.0	μΑ
Quiescent supply current	I <sub>CCT</sub>	Per Input Other Input	:V <sub>IN</sub> = 3.4 V :V <sub>CC</sub> or GND	5.5	_	_	1.35	_	1.50	mA

## AC Characteristics (input: $t_r = t_f = 3 \text{ ns}$ )

Characteristics	Symbol		Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
	Symbol	V <sub>CC</sub> (V)	C <sub>L</sub> (pF)	Min	Тур.	Max	Min	Max	Unit	
Propagation delay time	t <sub>pLH</sub> t <sub>pHL</sub>		5.0 ± 0.5	15		3.7	6.0	1.0	6.9	ns
				50		6.0	10.4	1.0	11.9	
3-state output enable time	t <sub>pZL</sub>	5.0	5.0 ± 0.5	15		3.6	5.6	1.0	6.5	ns
	t <sub>pZH</sub>			50		6.0	10.3	1.0	11.9	115
3-state output disable time	t <sub>pLZ</sub>		5.0 ± 0.5	50		7.3	10.0	1.0	11.5	ns
	$t_{pHZ}$		3.0 ± 0.5	30		7.0	10.0	1.0	11.5	113
Input capacitance	C <sub>IN</sub>				1	4	10			pF
Output capacitance	C <sub>OUT</sub>					6	_	_		pF
Power dissipation capacitance	$C_PD$			(Note 2)		14	_	_		pF

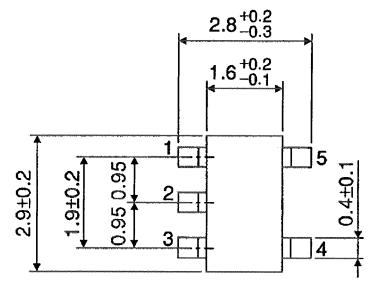
Note 2: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

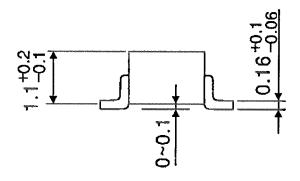
Average operating current can be obtained by the equation:

$$I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$$

## **Package Dimensions**

SSOP5-P-0.95 Unit: mm



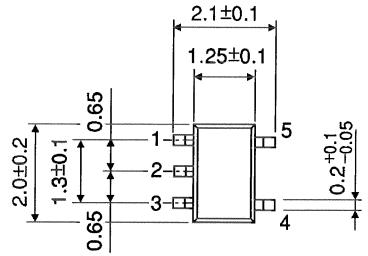


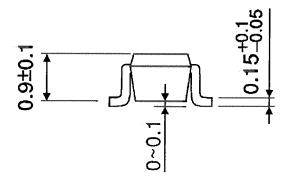
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Weight: 0.016 g (typ.)

## **Package Dimensions**

SSOP5-P-0.65A Unit: mm





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Weight: 0.006 g (typ.)

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